

Lab E5-VHDL Design Project

*EEE20001 Digital Electronics Design*

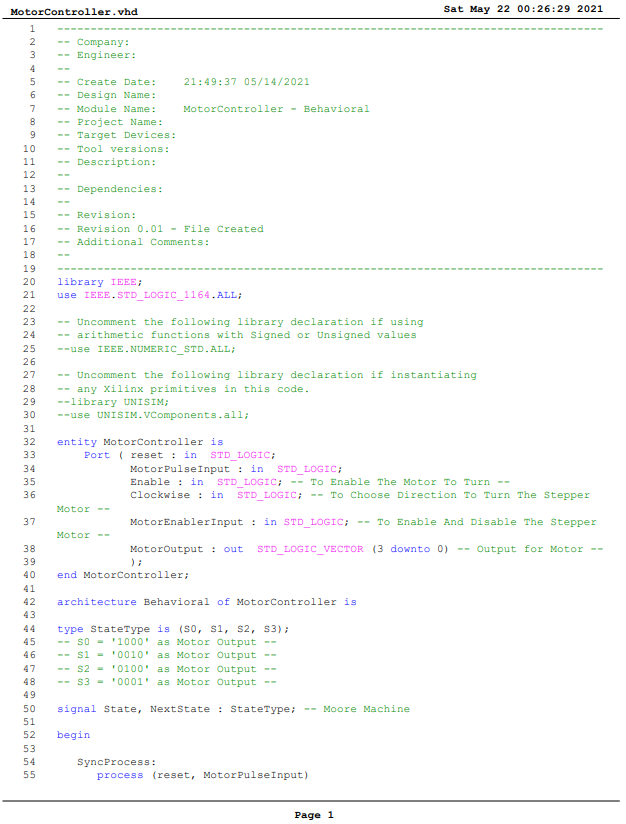
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| --- | --- | --- | --- | --- |
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| **2.** | **Student Name:** | **Sarah Lau Li Tin** | **Student ID:** | **101220896** |
| **3.** | **Student Name:** | **Lee Kai Ze** | **Student ID:** | **101222041** |

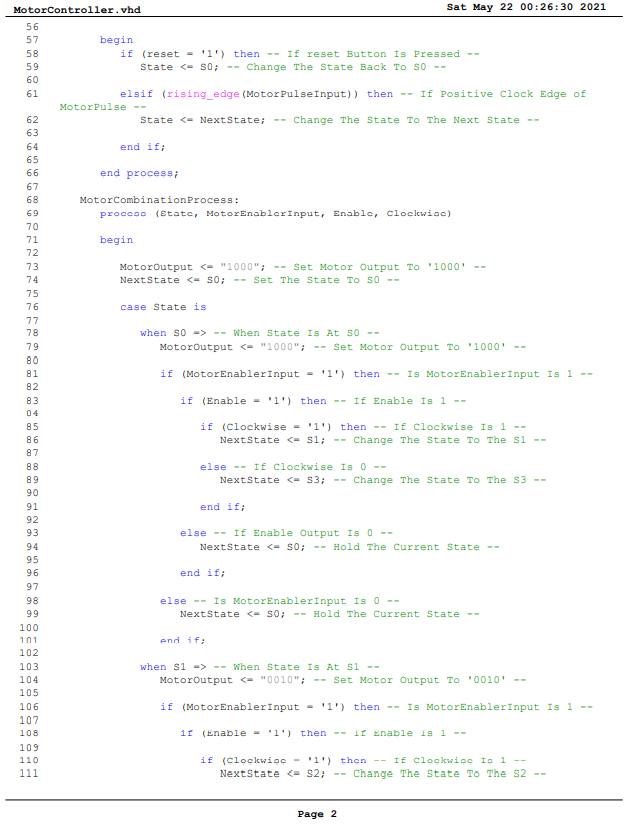
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| **Lab Supervisor Name:** | **Dr. Tay Fei Siang** |

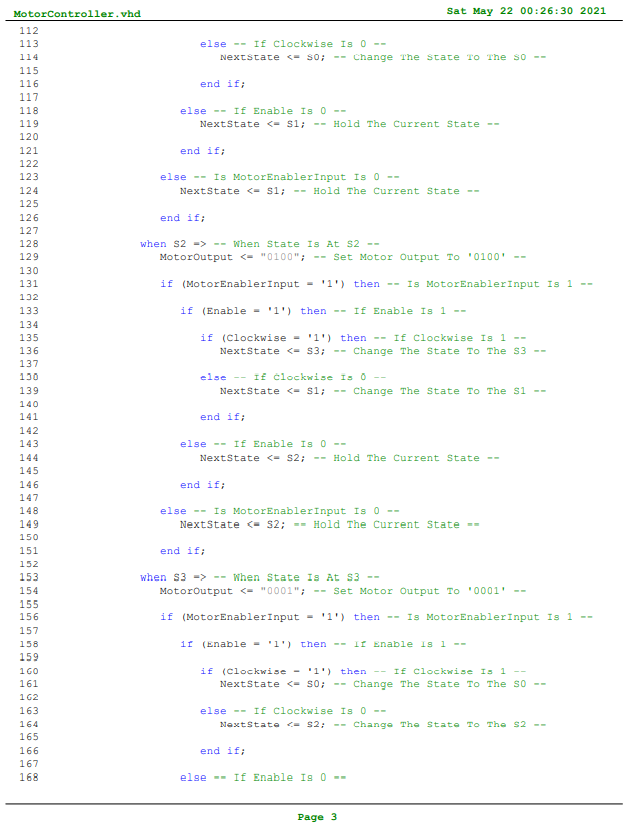


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Swinburne University of Technology Sarawak







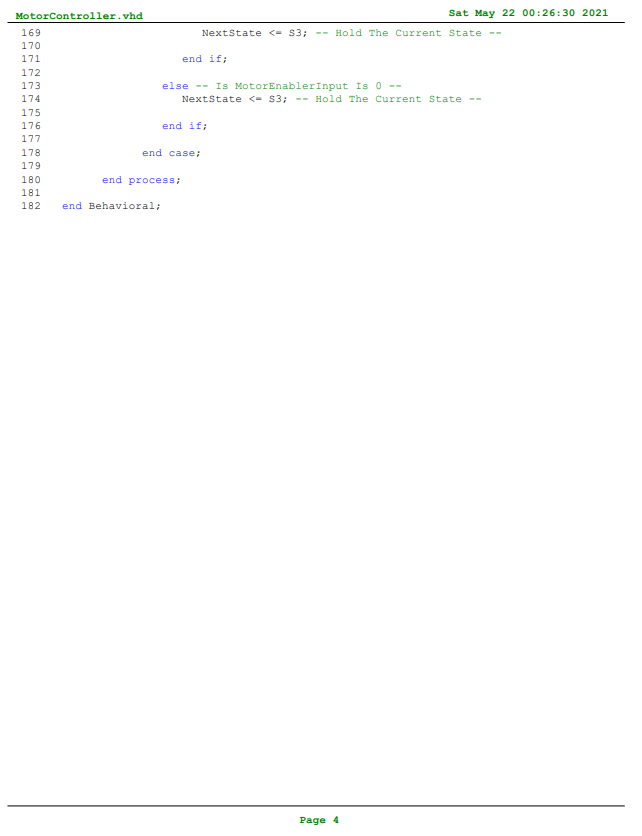
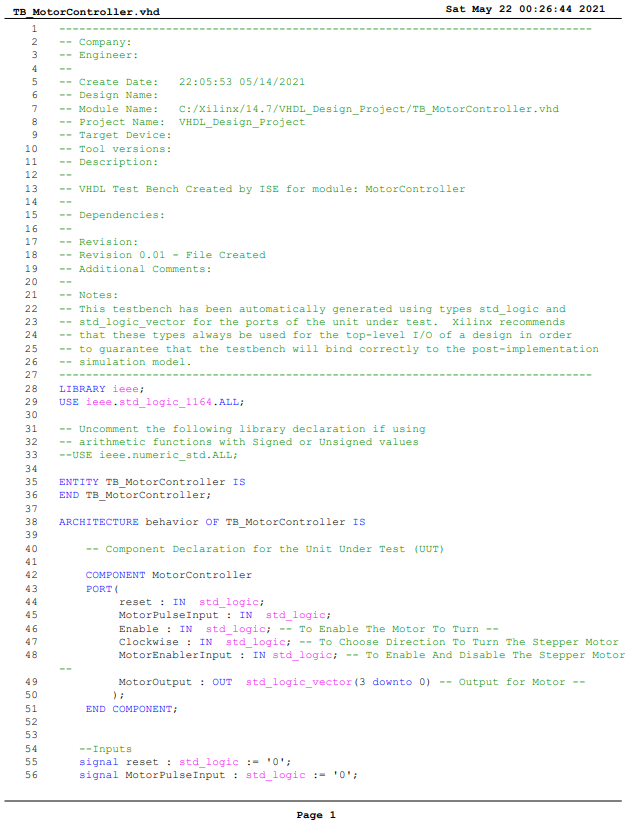
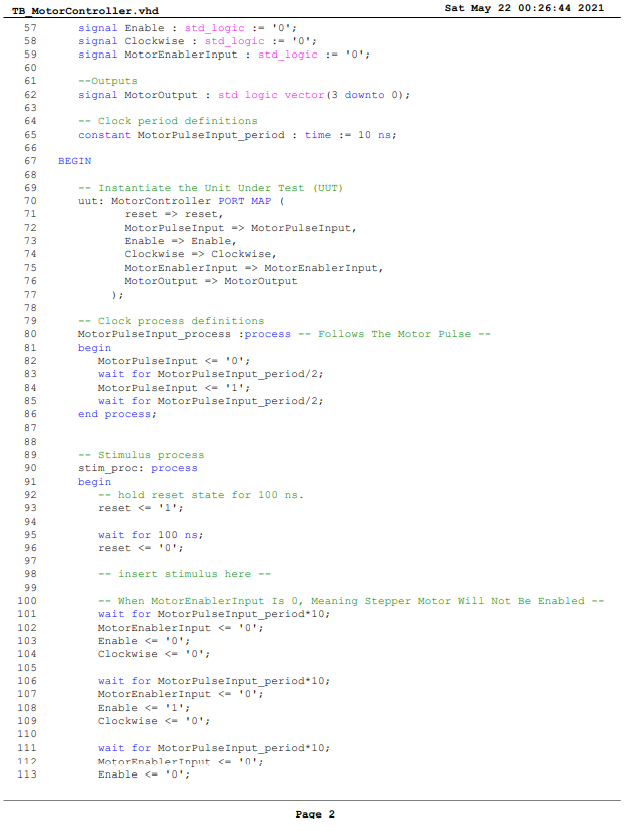


Figure : MotorController Module Code





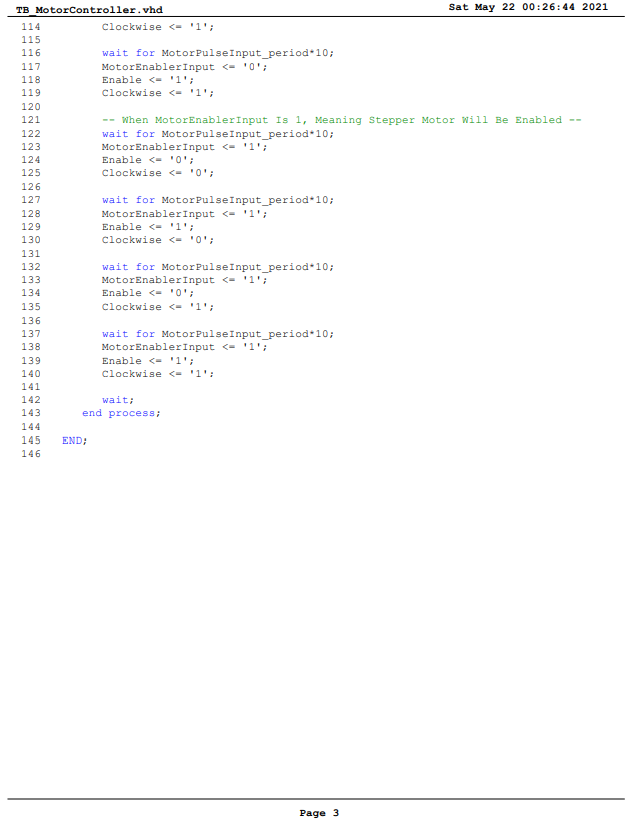
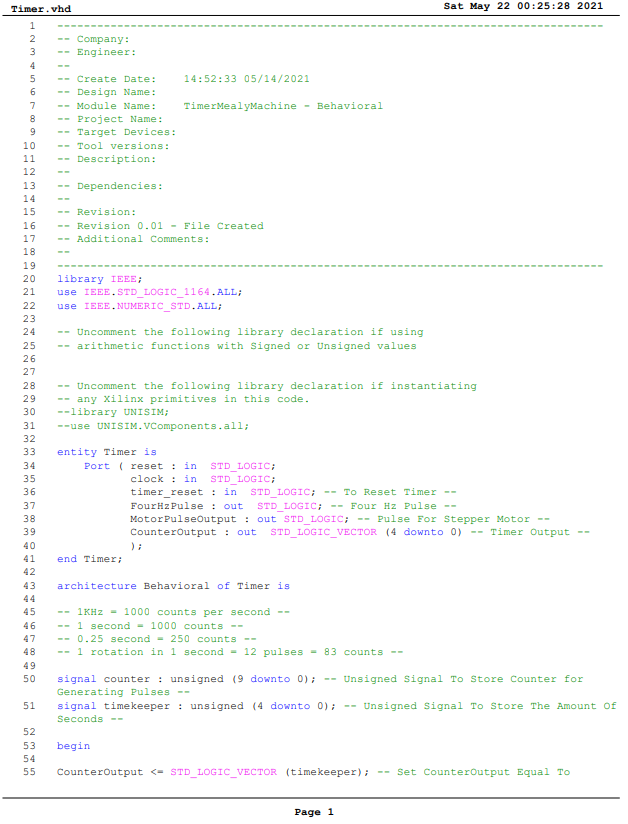


Figure : TB\_MotorController TestBench Code



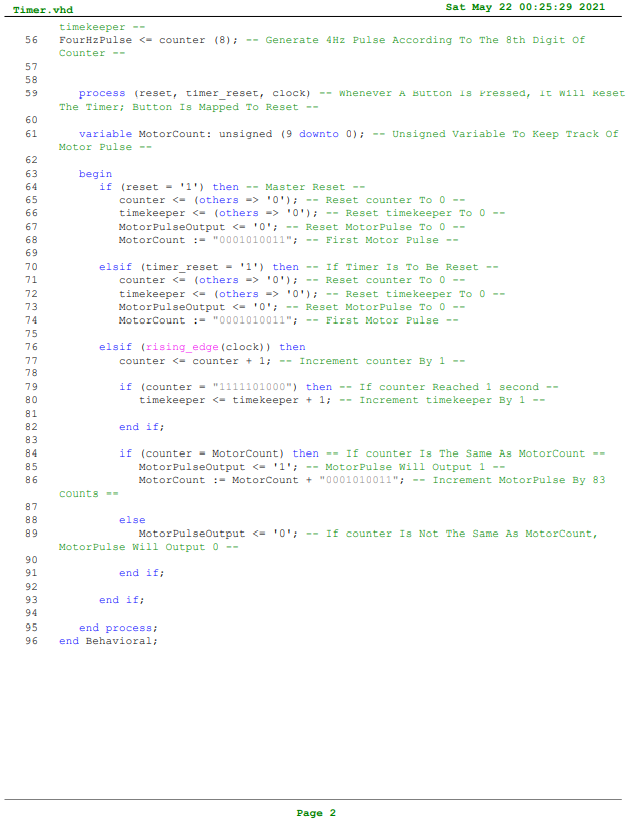
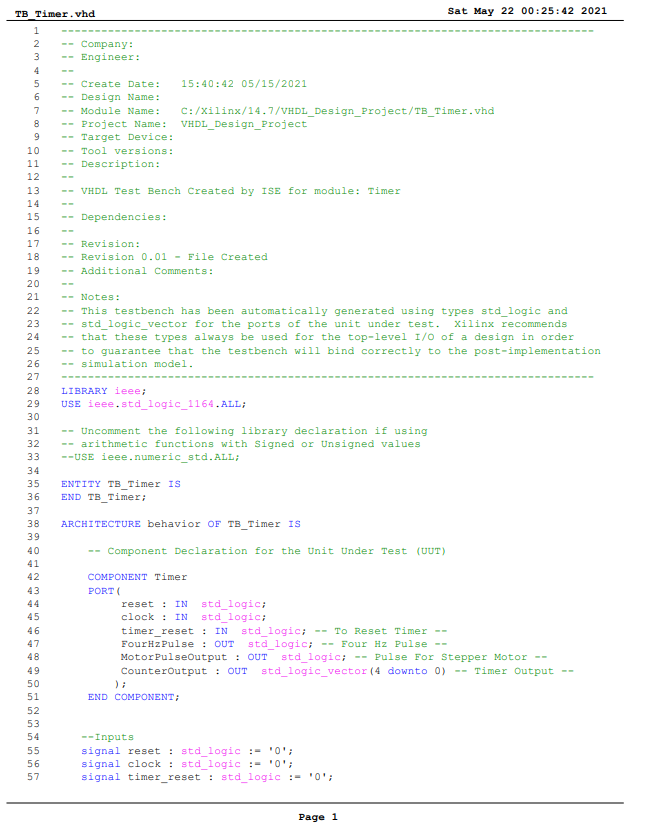


Figure : Timer Module Code



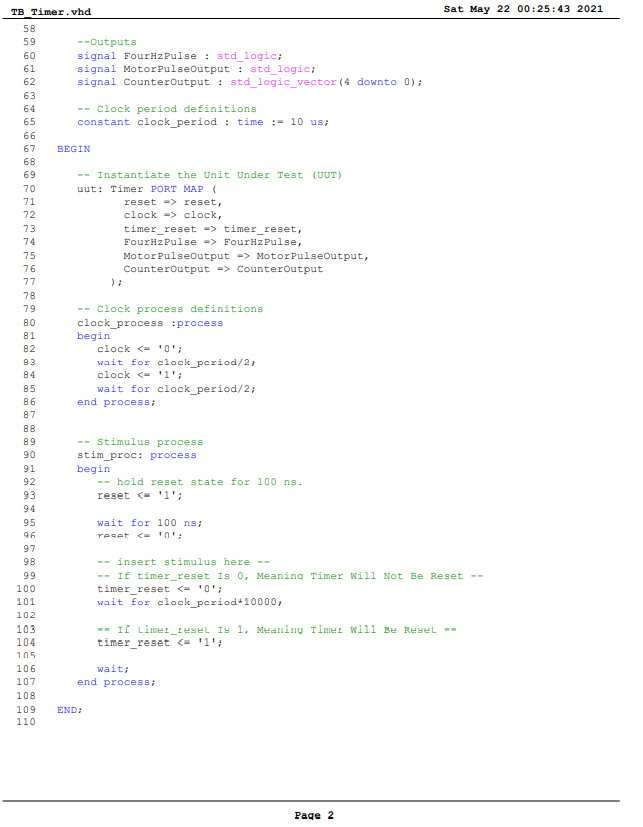
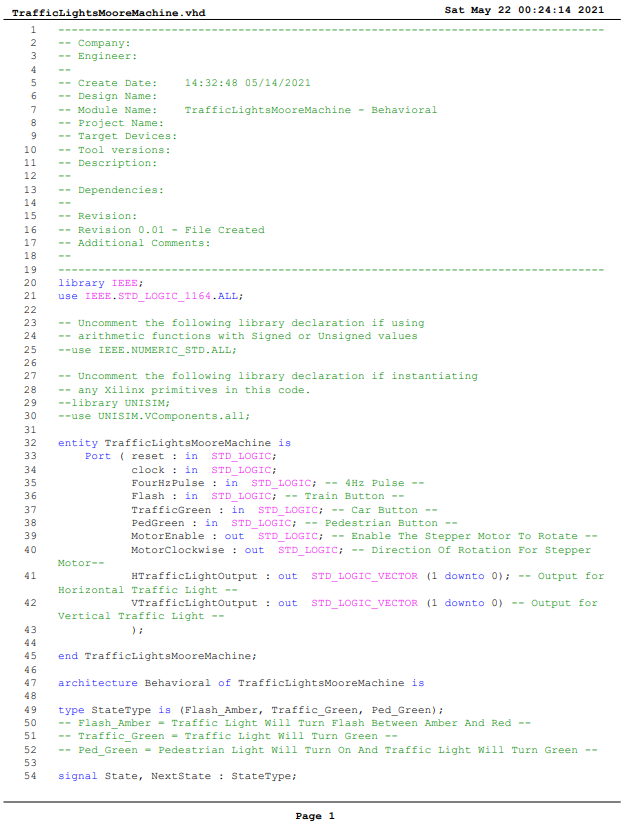
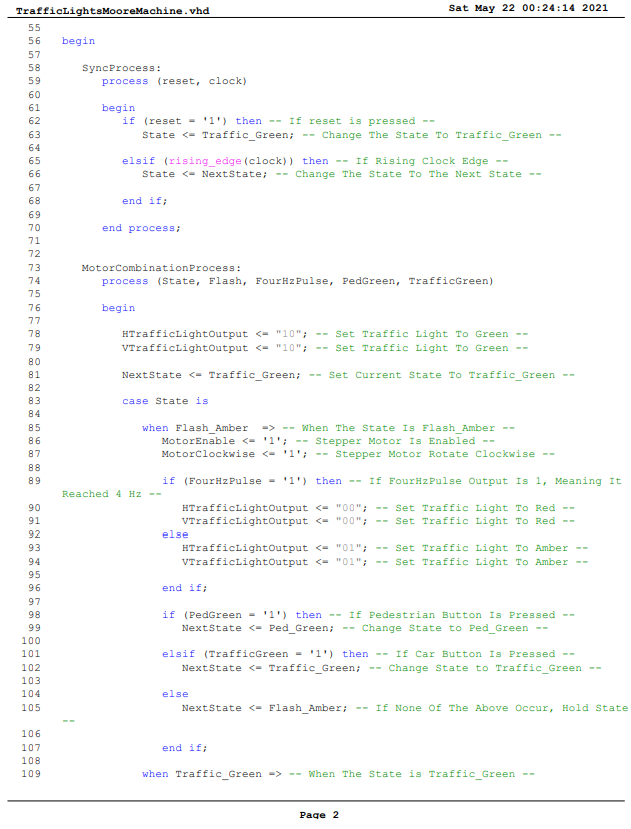


Figure : TB\_Timer TestBench Code





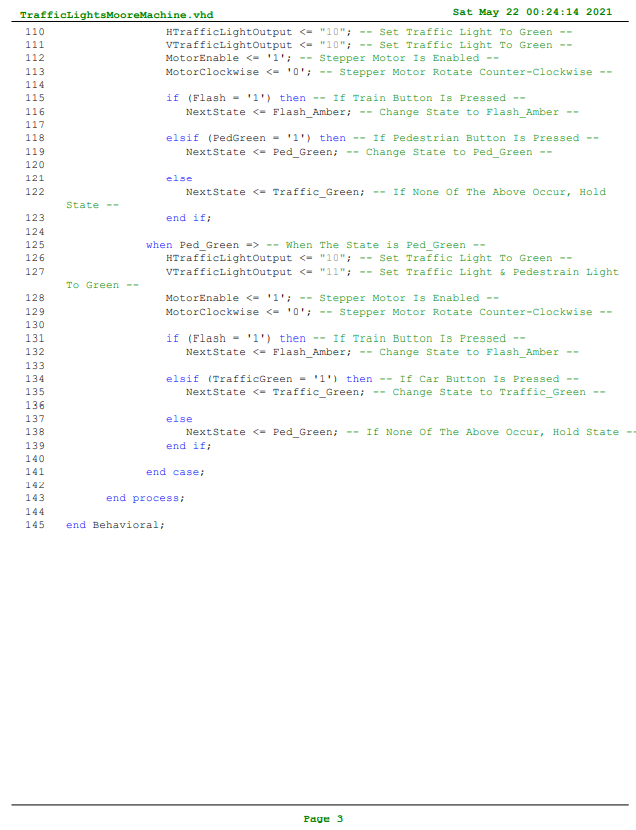
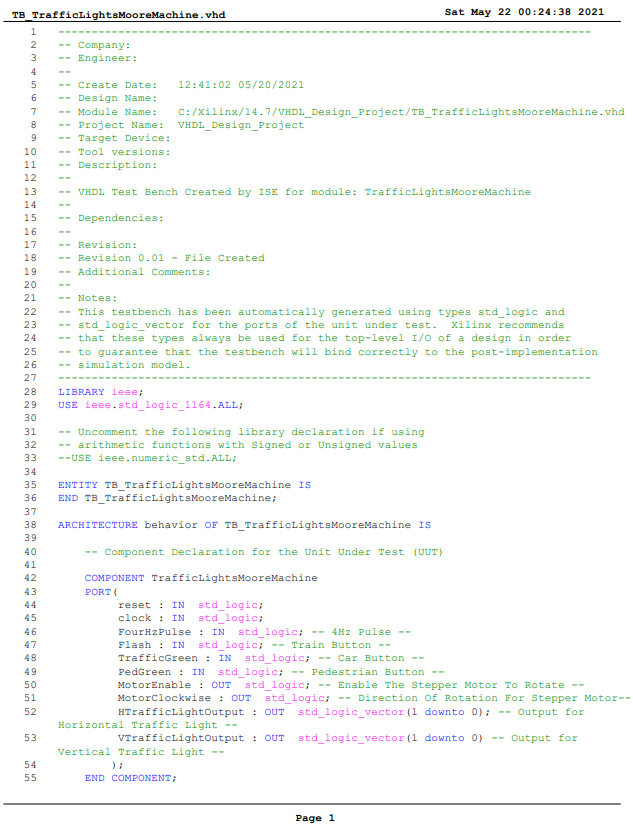
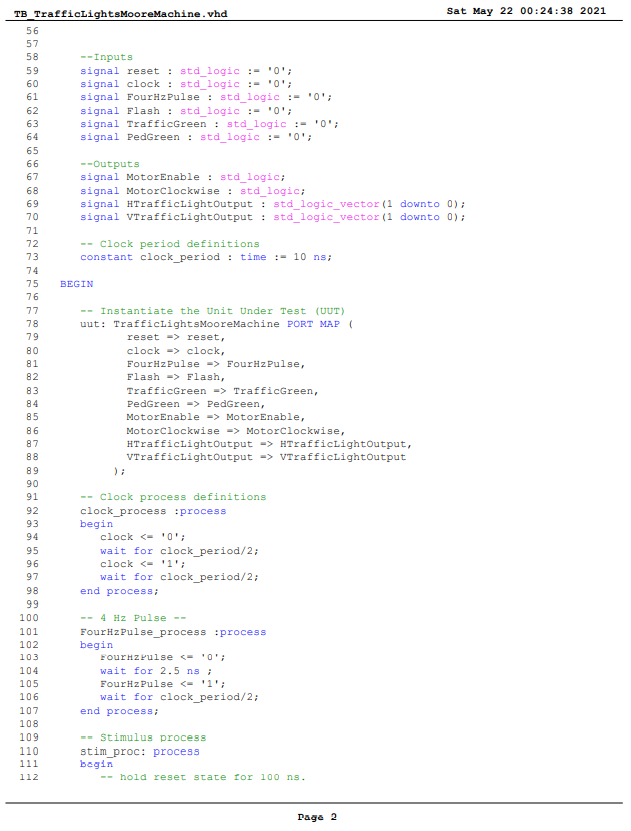


Figure : TrafficLightsMooreMachine Code





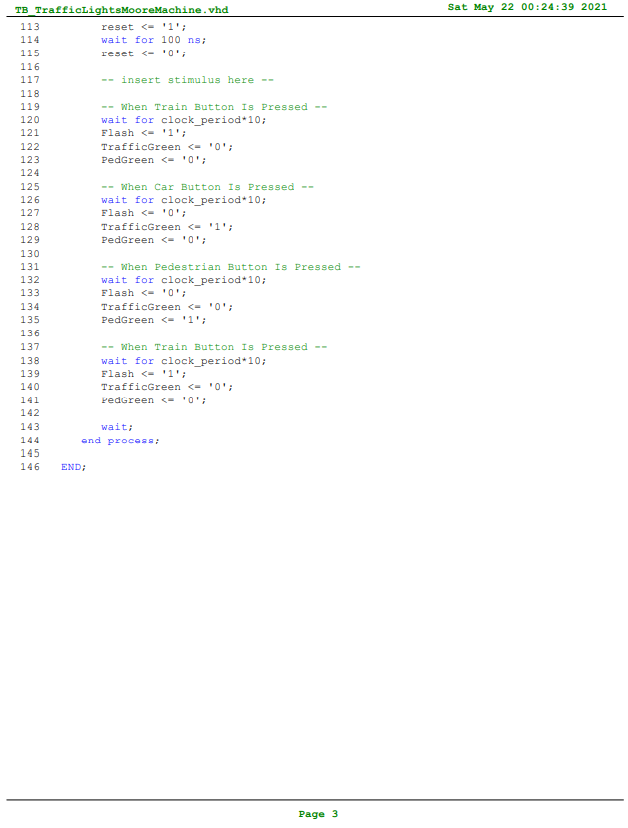
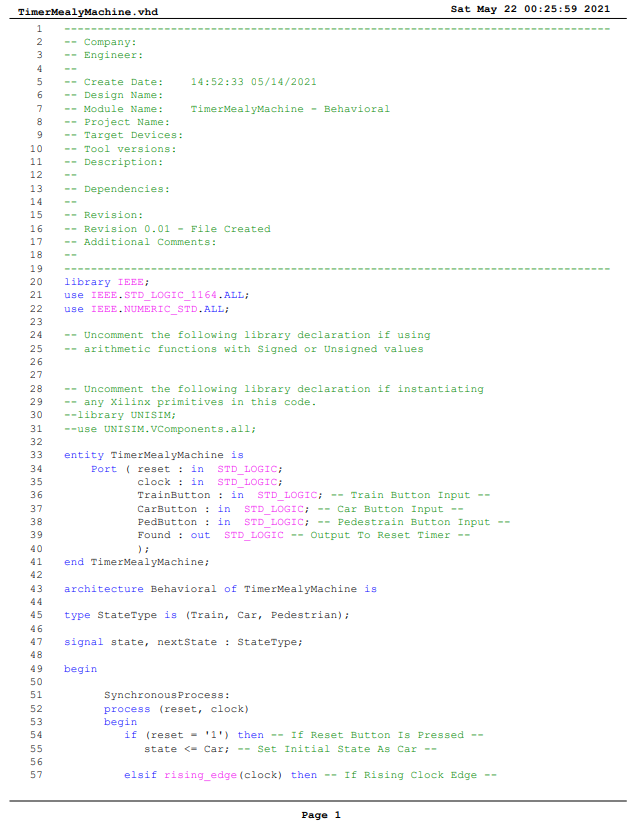


Figure : TB\_TrafficLightsMooreMachine TestBench Code



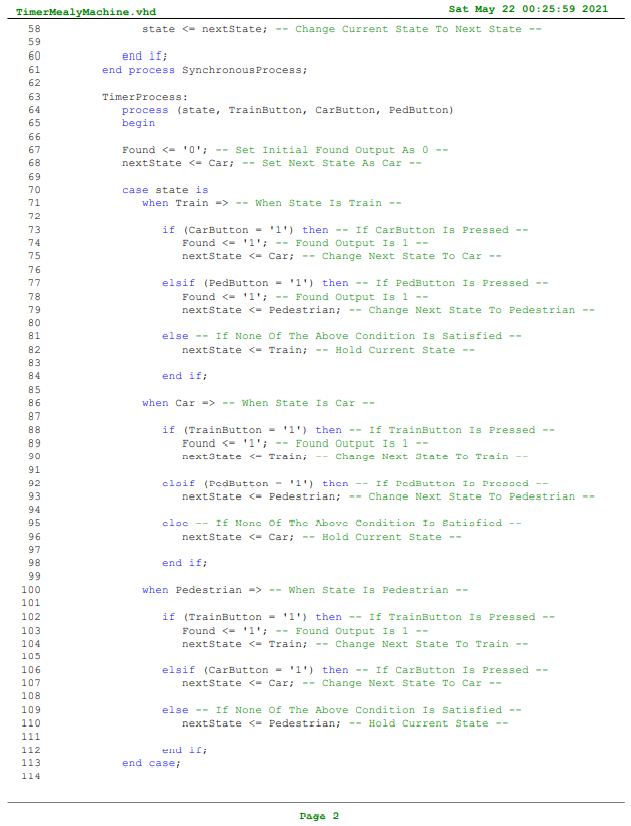
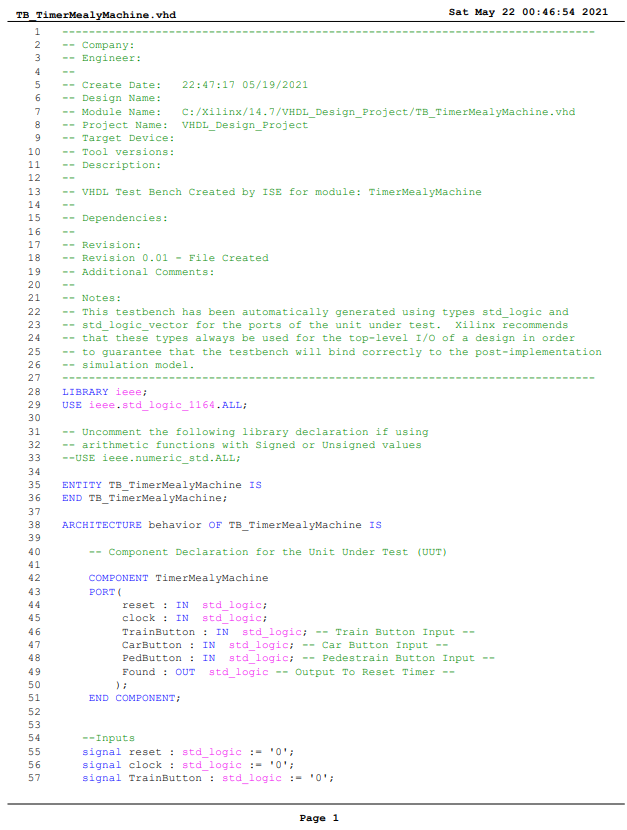
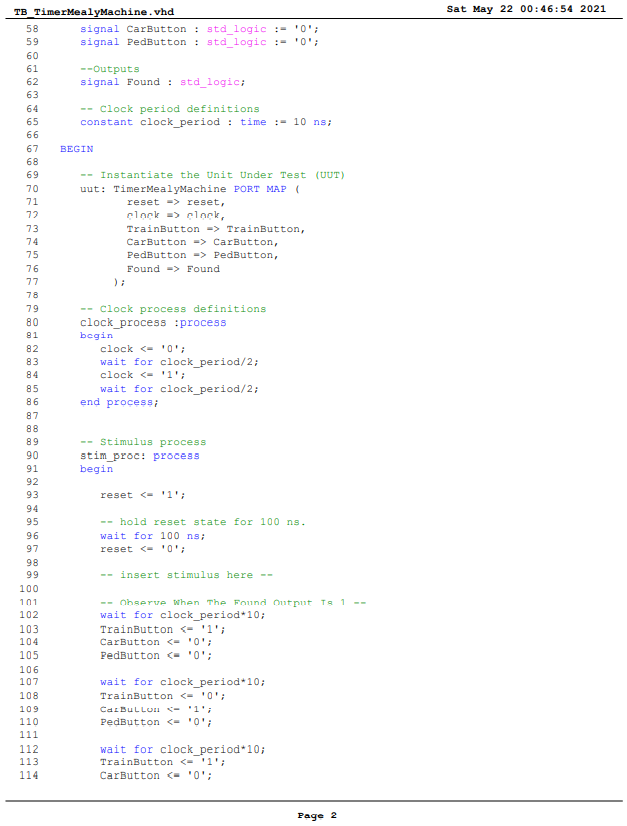




Figure : TimerMealyMachine Module Code





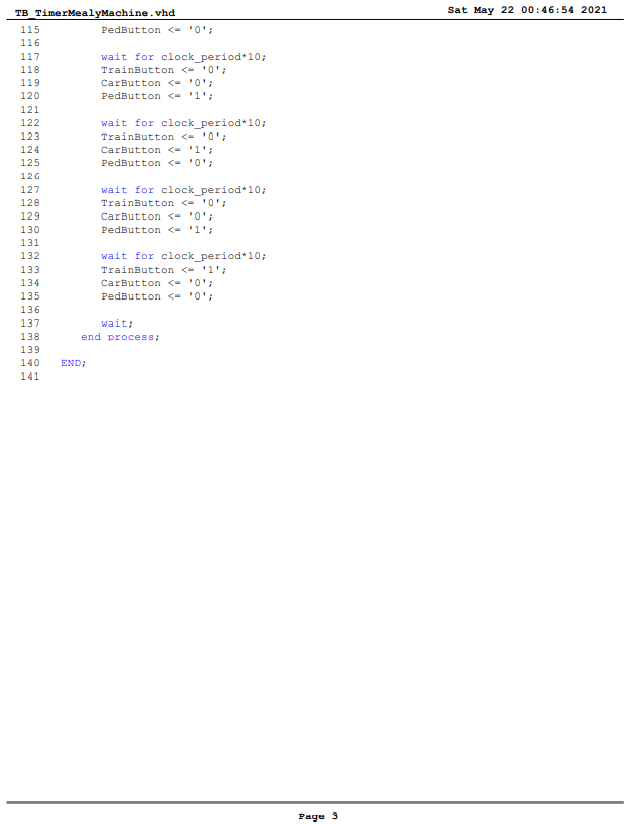


Figure : TB\_TimerMealyMachine TestBench Code